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INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

To Commissioner For Patents
Enclosed herewith is a Form PTO-1449, any required copies of
documents listed thereon, and any concise explanation of their
relevance is indicated below per 37 CFR 1.97.

Application Number	10/563483		
Filing Date			
First Named Inventor	PONOMAREV Youri		
Group Art Unit	2823		
Examiner Name	M. Estrada		
Atty. Docket Number	NL03 0833 US1		

U.S. PATENT DOCUMENTS					
Examiner Initials/7	Cite No.1	Document Number NoKind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns Lines, Where Relevant Passages or Relevant Figures Appear
MI	1	us- 4 665 412	05/12/1987	OHKAWA TIHIRO	
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				DATENT DOCUMENTS	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number (ctry³-no.⁴-kind⁵, if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of cited document	Pages, Columns Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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		33	NON-PATENT LITERATURE DOCUMENTS	
Exan		Cite No.	Include name of the author (in capital letters), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁰
M	R	1	KOJI Y ET AL: "THE FORMATION OF RESONANCE TUNNEL DIVICE BY GAMMA-AL203/SI "; EXTENDED ABSTRACTS OF THE INTERNATIONAL CONFERENCE ON SOLID STATE DEVICES AND MATERIALS; JAPAN SOCIETY OF APPLIED PHYSICS; TOKYO JA; Vol. 2001; 26-September 2001; pages 588-589	
		2	LU Z H ET AL: "CRYSTALLINE SI/SiO 2 QUANTUM WELLS"; APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS; NEW YORK, US; Vol. 80, No. 2; 14 January 2002; pages 255-257	
1	/	3	MEREU B ET AL: "FOWLER-NORDHEIM TUNNELING IN EPITAXIAL YTTRIUM OXIDE ON SILICON FOR HIGH-K GATE APPLICATIONS"; 2002 INTERNATIONAL SEMICONDUCTOR CONFERENCE; CAS 2002 PROCEEDINGS; IEEE PISCATAWAY; NJ USA; Vol. 2 8 Oct. 2002; pages 309-312	
Y	R	4	J. ALIEU ET AL: "MULTIPLE SIGE QUANTUM WELLS – NOVEL CHANNEL ARCHITECTURE FOR 0.2 CMOS"; PROCEEDINGS OF THE 29 TH EUROPEAN SOLID-STATE DEVICE RESEARCH CONFERENCE; LEUVEN, BELGUIM; 13-15 SEP. 1999; P. 292-295	

Examiner Signature	reale	Date Considered	2/2/08
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